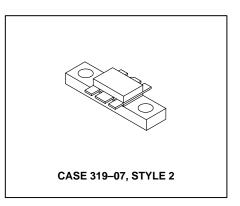
# The RF Line NPN Silicon RF Power Transistor

The MRF6409 is designed for GSM base stations applications. It incorporates high value emitter ballast resistors, gold metallizations and offers a high degree of reliability and ruggedness.

- To be used in Class AB
- Specified 26 Volts, 960 MHz Characteristics
   Output Power 20 Watts CW
   Gain 11 dB Typ
   Efficiency 60% Typ

# **MRF6409**

20 W, 960 MHz RF POWER TRANSISTOR NPN SILICON



## **MAXIMUM RATINGS**

Rating	Symbo	l Value	Unit
Collector–Emitter Voltage	VCEO	24	Vdc
Collector–Emitter Voltage	V <sub>CES</sub>	55	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector-Current — Continuous	l <sub>C</sub>	5.0	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	45 0.26	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Operating Junction Temperature	TJ	200	°C

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (1)	$R_{\theta JC}$	3.8	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 20 mAdc, I <sub>B</sub> = 0)	V(BR)CEO	24	30		Vdc
Emitter–Base Breakdown Voltage (I <sub>B</sub> = 5.0 mAdc, I <sub>C</sub> =0)	V(BR)EBO	4.0	5.0	_	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 20 mAdc, V <sub>BE</sub> = 0)	V(BR)CES	55	60	_	Vdc
Collector–Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>BE</sub> = 0)	ICES	_	-	6.0	mA

<sup>(1)</sup> Thermal resistance is determined under specified RF operating condition.



## **ELECTRICAL CHARACTERISTICS** — **continued** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
DC Current Gain (I <sub>CE</sub> = 1.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	hFE	20	35	80	_
DYNAMIC CHARACTERISTICS		•			
Output Capacitance (V <sub>CB</sub> = 26 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	18	_	pF
FUNCTIONAL TESTS					
Common–Emitter Amplifier Power Gain (V <sub>CC</sub> = 26 Vdc, P <sub>Out</sub> = 20 W (CW), I <sub>CQ</sub> = 50 mA, f = 960 MHz)	G <sub>pe</sub>	10	11	_	dB
Collector Efficiency (V <sub>CC</sub> = 26 Vdc, P <sub>out</sub> = 20 W (CW), I <sub>CQ</sub> = 50 mA, f = 960 MHz)	η	50	60	_	%
Load Mismatch (V <sub>CC</sub> = 26 Vdc, P <sub>out</sub> = 15 W (CW), I <sub>CQ</sub> = 50 mA, f = 960 MHz, Load VSWR = 3:1, All Phase Angles at Frequency of Test)	Ψ	No Degradation in Output Power			

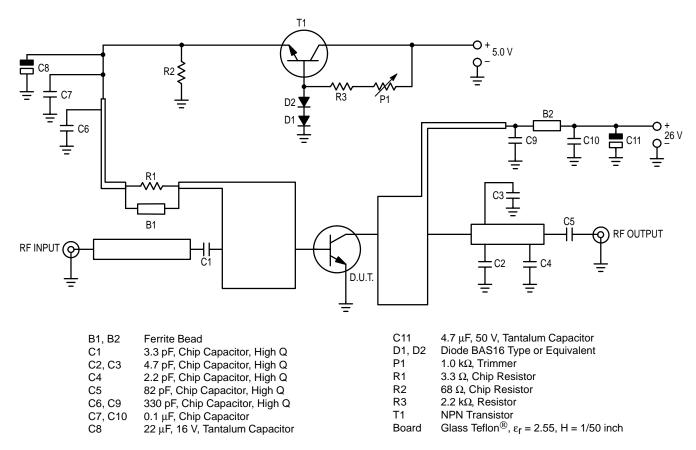
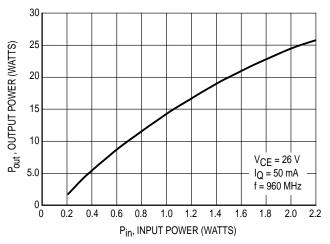


Figure 1. Test Circuit Electrical Schematic

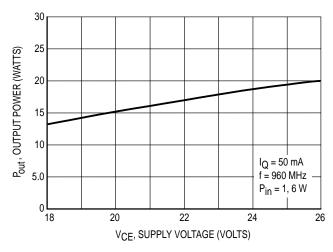
# **TYPICAL CHARACTERISTICS**



25 24 P<sub>out</sub>, OUTPUT POWER (WATTS) 23 22 21 20 19 18 V<sub>CE</sub> = 26 V  $I_Q = 50 \text{ mA}$ 17  $P_{in} = 1 W$ 16 15 920 930 940 950 960 970 f, FREQUENCY (MHz)

Figure 2. Output Power versus Input Power (CW)

Figure 3. Output Power versus Frequency (CW)



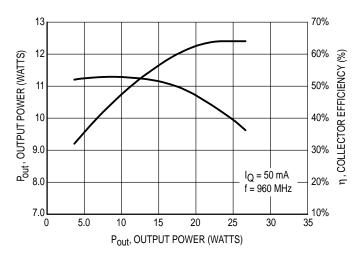


Figure 4. Output Power versus Supply Voltage (CW)

Figure 5. Power Gain and Efficiency versus Output Power

V<sub>CE</sub> = 26 V

 $I_{CQ} = 50 \text{ mA}$ 

f1 = 960 MHz

f2 = 960, 1 MHz

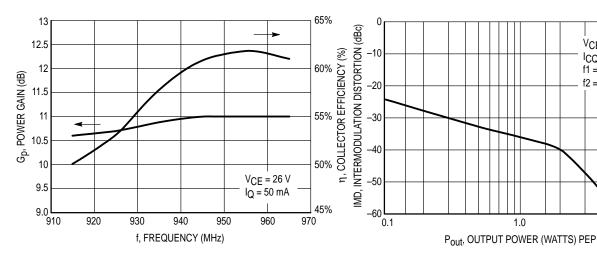
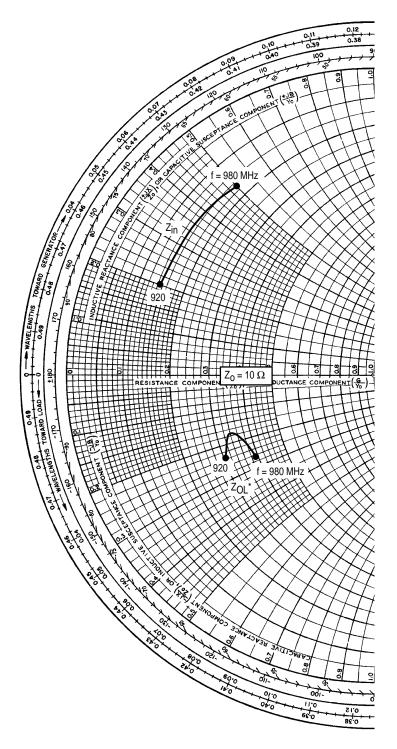


Figure 6. Typical Broadband Performances

Figure 7. Intermodulation Distortion versus Output Power

10



f (MHz)	Z <sub>in</sub> (Ω)	Z <sub>OL</sub> * (Ω)
920	1.4 + j3.0	3.2 – j2.5
940	1.5 + j3.9	3.5 – j1.88
960	1.5 + j4.2	3.9 – j2.5
980	1.6 + j4.4	4.0 – j2.8

Z<sub>OL</sub>\*: Conjugate of optimum load impedance into which the device operates at a given output power, voltage, current and frequency.

Figure 8. Input and Output Impedances with Circuit Tuned for Maximum Gain @  $V_{CC}$  = 26 V,  $I_{CQ}$  = 50 mA,  $P_{out}$  = 20 W (CW)

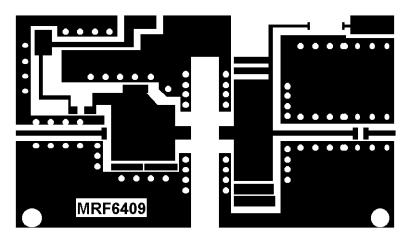


Figure 9. 960 MHz Test Circuit RF, Photomaster Scale 1:1 (Reduced 25% in printed data book, DL110/D)

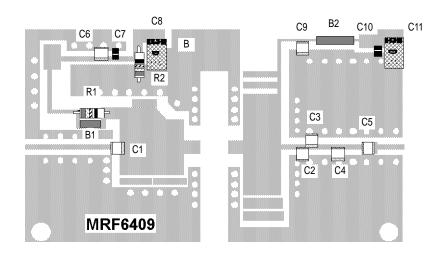
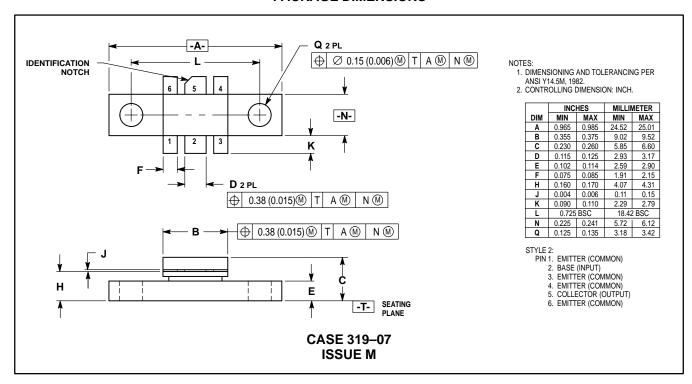


Figure 10. 960 MHz Test Circuit RF, Photomaster Scale 1:1 and Components Location (Reduced 25% in printed data book, DL110/D)

MOTOROLA RF DEVICE DATA MRF6409

#### PACKAGE DIMENSIONS



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