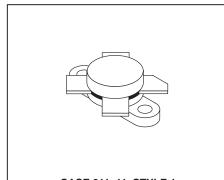
The RF Line NPN Silicon RF Power Transistor

Designed primarily for application as a high–power linear amplifier from 2.0 to 30 MHz.

- Specified 12.5 Volt, 30 MHz Characteristics —
 Output Power = 100 W (PEP)
 Minimum Gain = 10 dB
 Efficiency = 40%
- Intermodulation Distortion @ 100 W (PEP) IMD = -30 dB (Min)
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR

MRF421

100 W (PEP), 30 MHz RF POWER TRANSISTORS NPN SILICON



CASE 211-11, STYLE 1

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	20	Vdc
Collector–Base Voltage	V _{СВО}	45	Vdc
Emitter-Base Voltage	V _{EBO}	3.0	Vdc
Collector Current — Continuous	IC	20	Adc
Withstand Current — 10 s	_	30	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	290 1.66	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.6	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

	Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
	Collector–Emitter Breakdown Voltage ($I_C = 50 \text{ mAdc}, I_B = 0$)	V(BR)CEO	20		-	Vdc
	Collector–Emitter Breakdown Voltage (I _C = 200 mAdc, V _{BE} = 0)	V(BR)CES	45		1	Vdc
	Collector-Base Breakdown Voltage (I _C = 200 mAdc, I _E = 0)	V(BR)CBO	45		1	Vdc
	Emitter–Base Breakdown Voltage ($I_E = 10 \text{ mAdc}, I_C = 0$)	V(BR)EBO	3.0	_	_	Vdc
	Collector Cutoff Current (V _{CE} = 16 Vdc, V _{BE} = 0, T _C = 25°C)	ICES	_	_	10	mAdc

(continued)



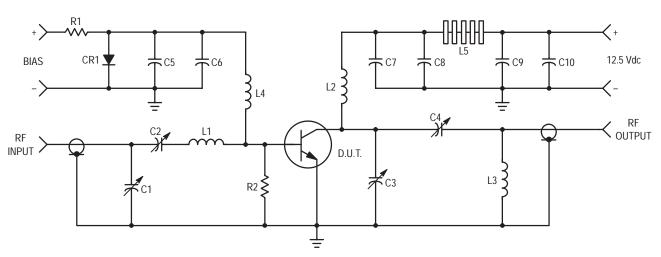


ELECTRICAL CHARACTERISTICS – continued (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
DC Current Gain (IC = 5.0 Adc, VCE = 5.0 Vdc)	h _{FE}	10	70	_	_
DYNAMIC CHARACTERISTICS					•
Output Capacitance (V _{CB} = 12.5 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	550	800	pF
FUNCTIONAL TESTS	•				
Common–Emitter Amplifier Power Gain $(V_{CC} = 12.5 \text{ Vdc}, P_{out} = 100 \text{ W}, I_{C(max)} = 10 \text{ Adc}, I_{CQ} = 150 \text{ mAdc}, f = 30, 30.001 \text{ MHz})$	GPE	10	12	_	dB
Collector Efficiency $(V_{CC} = 12.5 \text{ Vdc}, P_{out} = 100 \text{ W}, I_{C(max)} = 10 \text{ Adc}, I_{CQ} = 150 \text{ mA}, f = 30, 30.001 \text{ MHz})$	η	40	_	_	%
Intermodulation Distortion (1) (V _{CE} = 12.5 Vdc, P _{out} = 100 W, I _C = 10 Adc, I _{CQ} = 150 mA, f = 30, 30.001 MHz)	IMD	_	-33	-30	dB

NOTE:

NOT RECOMMENDED FOR NEW DESIGN



C1, C2, C4 — 170-780 pF, ARCO 469

C3 — 80-480 pF, ARCO 466

C5, C7, C10 — ERIE 0.1 $\mu\text{F},\,100~\text{V}$

 $C6 - MALLORY 500 \, \mu F$ @ 15 V Electrolytic

 $C9 - 100 \mu F$, 15 V Electrolytic

C8 — 1000 pF, 350 V UNDERWOOD

R1 — 10 Ω , 25 Watt Wirewound

 $R2 - 10 \Omega$, 1.0 Watt Carbon

CR1 - 1N4997

L1 — 3 Turns, #16 Wire, 5/16" I.D., 5/16" Long

L2-12 Turns, #16 Enameled Wire Closewound, $1/4^{\prime\prime}$ I.D.

L3 — 1–3/4 Turns, 1/8" Tubing, 3/8" I.D., 3/8" Long

L4 — 10 μH Molded Choke

L5 — 10 Ferrite Beads — FERROXCUBE #56–590–65/3B

Figure 1. 30 MHz Test Circuit Schematic

^{1.} To proposed EIA method of measurement. Reference peak envelope power.

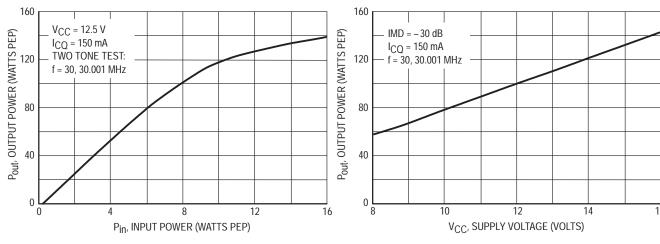


Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Supply Voltage

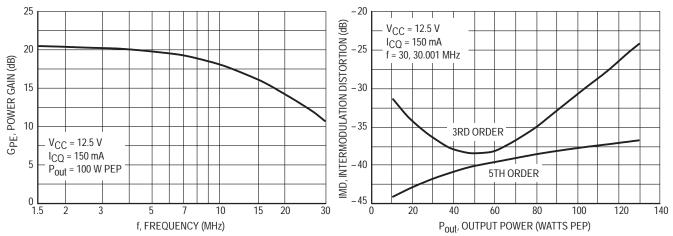


Figure 4. Power Gain versus Frequency

Figure 5. Intermodulation Distortion versus Output Power

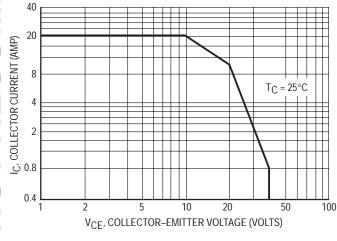


Figure 6. DC Safe Operating Area

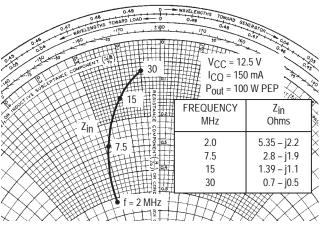


Figure 7. Series Equivalent Impedance

MOTOROLA RF DEVICE DATA **MRF421**

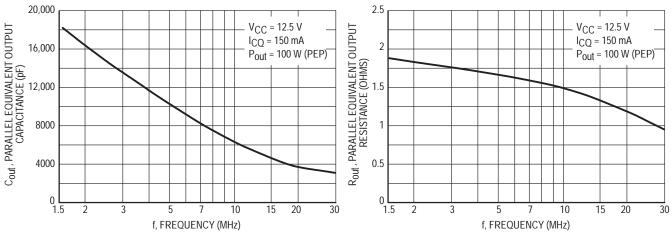
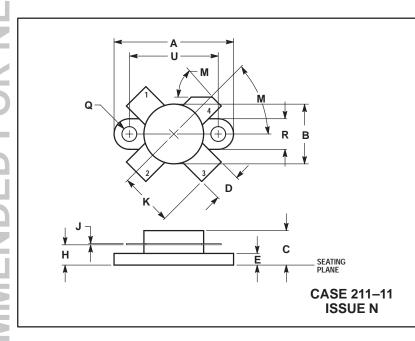


Figure 8. Output Capacitance versus Frequency

Figure 9. Output Resistance versus Frequency

PACKAGE DIMENSIONS



- 1. DIMENSIONING AND TOLERANCING PER ANSI
 - 2. CONTROLLING DIMENSION: INCH

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.960	0.990	24.39	25.14	
В	0.465	0.510	11.82	12.95 6.98 5.96	
С	0.229	0.275	5.82		
D	0.216	0.235	5.49		
E	0.084	0.110	2.14	2.79 4.52 0.17	
Н	0.144	0.178	3.66		
J	0.003	0.007	0.08		
K	0.435		11.05		
M	45°	45 °NOM		NOM	
Q	0.115	0.130	2.93	3.30	
R	0.246	0.255	6.25	6.47	
U	0.720	0.730	18.29	18.54	

STYLE 1: PIN 1. EMITTER

BASE EMITTER

4. COLLECTOR

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