

# **1.1 GHz Super Low Power Dual Modulus Prescaler**

The MC12052A is a super low power dual modulus prescaler used in phase–locked loop applications. Motorola's advanced Bipolar MOSAIC<sup>™</sup> V technology is utilized to achieve low power dissipation of 2.7 mW at a minimum supply voltage of 2.7 V.

The MC12052A can be used with CMOS synthesizers requiring positive edges to trigger internal counters such as Motorola's MC145XXX series in a PLL to provide tuning signals up to 1.1 GHz in programmable frequency steps.

A Divide Ratio Control (SW) permits selection of a 64/65 or 128/129 divide ratio as desired.

The Modulus Control (MC) selects the proper divide number after SW has been biased to select the desired divide ratio.

- 1.1 GHz Toggle Frequency
- The MC12052 is Pin and Functionally Compatible with the MC12022
- Low Power 1.0 mA Typical
- 2.0 mA Maximum, -40 to 85°C, V<sub>CC</sub> = 2.7 to 5.5 Vdc
- Short Setup Time (tset) 16 ns Maximum @ 1.1 GHz
- Modulus Control Input Level is Compatible with Standard CMOS and TTL
- Maximum Input Voltage Should Be Limited to 6.5 Vdc

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#### FUNCTIONAL TABLE

SW	MC	Divide Ratio
Н	Н	64
Н	L	65
L	н	128
L	L	129

**NOTES:** 1. SW:  $H = V_{CC}$ , L = Open. A logic L can also be applied by grounding this pin, but this is not recommended due to increased power consumption. 2. MC:  $H = 2.0 \text{ V to } V_{CC}$ , L = GND to 0.8 V.

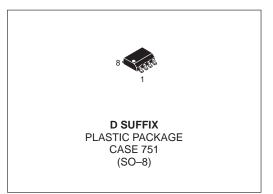
#### MAXIMUM RATINGS

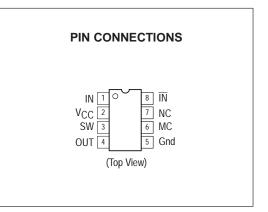
Characteristic	Symbol	Range	Unit
Power Supply Voltage, Pin 2	VCC	-0.5 to 7.0	Vdc
Operating Temperature Range	TA	-40 to 85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to 150	°C
Modulus Control Input, Pin 6	MC	-0.5 to 6.5	Vdc

## MC12052A

## MECL PLL COMPONENTS ÷64/65, ÷128/129 LOW POWER DUAL MODULUS PRESCALER

SEMICONDUCTOR TECHNICAL DATA





#### ORDERING INFORMATION

Device		Operating Temp Range	Package	
	MC12052AD	T <sub>A</sub> =−40 to 85°C	SO–8	

### MC12052A

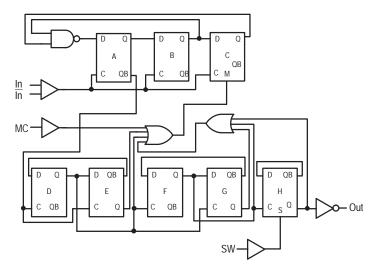
**ELECTRICAL CHARACTERISTICS** (V<sub>CC</sub> = 2.7 to 5.5 VDC,  $T_A = -40$  to 85°C, unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)	ft	0.1	1.4	1.1	GHz
Supply Current (Pin 2)	ICC	-	1.0	2.0	mA
Modulus Control Input High (MC)	VIH1	2.0	_	V <sub>CC</sub> + 0.5 V	V
Modulus Control Input Low (MC)	VIL1	Gnd	_	0.8	V
Divide Ratio Control Input High (SW)	V <sub>IH2</sub>	$V_{CC} - 0.5 V$	VCC	V <sub>CC</sub> + 0.5 V	VDC
Divide Ratio Control Input Low (SW)	V <sub>IL2</sub>	Open	Open	Open	-
Output Voltage Swing (Note 2) ( $C_L = 8.0 \text{ pF}, R_L = 3.3 \text{ k}\Omega$ )	Vout	0.8	1.1	_	Vpp
Modulus Setup Time MC to Out @ 1100 MHz	t <sub>set</sub>	-	11	16	ns
Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	Vin	100 400	-	1000 1000	mV <sub>PP</sub>
Output Current (Note 1) $V_{CC} = 2.7 \text{ V}, \text{ CL} = 8.0 \text{ pF}, \text{ RL} = 3.3 \text{ k}\Omega$ $V_{CC} = 5.0 \text{ V}, \text{ CL} = 8.0 \text{ pF}, \text{ RL} = 7.2 \text{ k}\Omega$	IO		0.5 0.5	3.0 3.0	mA

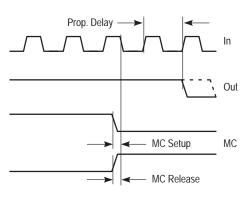
NOTES: 1. Divide ratio of ÷64/65 @ 1.1 GHz

2. Valid over voltage range 2.7 to 5.5 V; RL = 3.3 k $\Omega$  @ V\_{CC} = 2.7 V; RL = 7.2 k $\Omega$  @ V\_{CC} = 5.0 V

#### Figure 1. Logic Diagram (MC12052A)

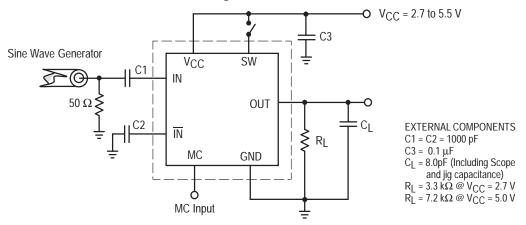


#### Figure 2. Modulus Setup Time



Modulus setup time MC to out is the MC setup or MC release plus the prop delay.

#### Figure 3. AC Test Circuit



## MC12052A

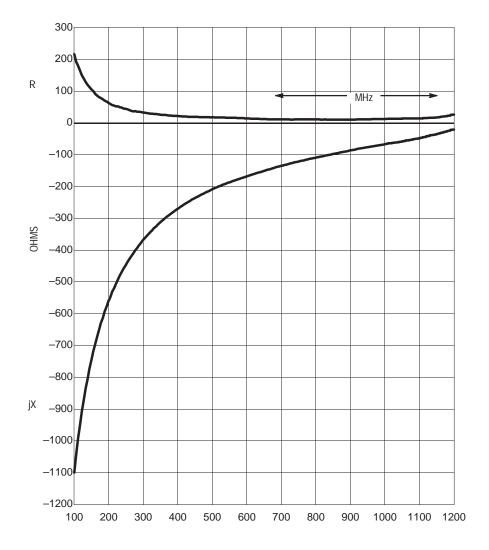
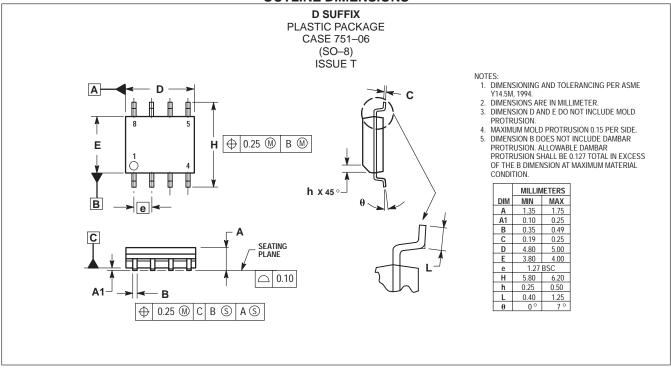


Figure 4. Typical Input Impedance versus Input Frequency

### MC12052A





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