

# 1.1 GHz Dual Modulus Prescaler

The MC12028A can be used with CMOS synthesizers requiring positive edges to trigger internal counters such as Motorola's MC145XXX series in a PLL to provide tuning signals up to 1.1 GHz in programmable frequency steps.

The MC12028B can be used with CMOS synthesizers requiring negative edges to trigger internal counters.

A Divide Ratio Control (SW) permits selection of a 32/33 or 64/65 divide ratio as desired.

The Modulus Control (MC) selects the proper divide number after SW has been biased to select the desired divide ratio.

#### NOTE: The "B" Version Is Not Recommended for New Designs

- 1.1 GHz Toggle Frequency
- MC12028A for Positive Edge Triggered Synthesizers
- 6.5 mA Maximum, -40 to 85°C, V<sub>CC</sub> = 5.5 Vdc
- Modulus Control Input Level Is Compatible With Standard CMOS and TTL
- Low–Power 4.0 mA Typical

#### FUNCTIONAL TABLE

	SW	MC	Divide Ratio		
	Н	н	32		
	н	L	33		
	L	н	64		
	L	L	65		

NOTES: 1. SW: H = V<sub>CC</sub>, L = Open. A logic L can also be applied by grouunding this pin, but this is not recommended due to increased power soncumption. 2. MC: H = 2.0 V to V<sub>CC</sub>, L = Gnd to 0.8 V.

#### **DESIGN GUIDE**

Criteria	Value	Unit
Internal Gate Count*	67	ea
Internal Gate Propagation Delay	200	ps
Internal Gate Power Dissipation	0.75	mW
Speed Power Product	0.15	рJ

NOTE: \* Equivalent to a two-input NAND gate

#### MAXIMUM RATINGS

Characteristic	Symbol	Range	Unit
Power Supply Voltage, Pin 2	VCC	-0.5 to 7.0	Vdc
Operating Temperature Range	TA	-40 to 85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to 150	°C
Modulus Control Input, Pin 6	MC	-0.5 to 6.5	Vdc

NOTE: ESD data available upon request.

# MC12028A MC12028B

MECL PLL COMPONENTS ÷32/33, ÷64/65 DUAL MODULUS PRESCALER

> SEMICONDUCTOR TECHNICAL DATA

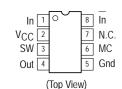


D SUFFIX PLASTIC PACKAGE CASE 751 (SO–8)



P SUFFIX PLASTIC PACKAGE CASE 626

#### **PIN CONNECTIONS**

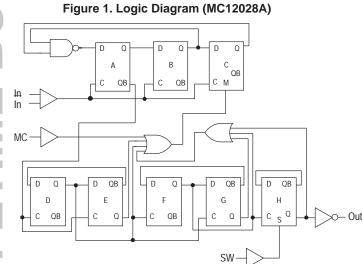


#### **ORDERING INFORMATION**

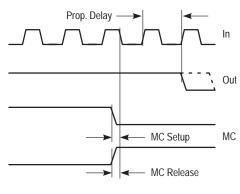
Device	Operating Temp Range	Package		
MC12028AD	T <sub>A</sub> =–40° to 85°C	SO–8		
MC12028AP	1A =-40 10 85 C	Plastic		

**ELECTRICAL CHARACTERISTICS** (V<sub>CC</sub> = 4.5 to 5.5V;  $T_A = -40$  to 85°C, unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)	ft	0.1	1.4	1.1	GHz
Supply Current Output Unloaded (Pin 2)	ICC	-	4.0	6.5	mA
Modulus Control Input High (MC)	VIH1	2.0	-	VCC	V
Modulus Control Input Low (MC)	VIL1	-	-	0.8	V
Divide Ratio Control Input High (SW)	VIH2	VCC	Vcc	VCC	Vdc
Divide Ratio Control Input Low (SW)	V <sub>IL2</sub>	Open	Open	Open	-
Output Voltage Swing (C <sub>L</sub> = 12 pF; R <sub>L</sub> = 2.2 k $\Omega$ )	Vout	1.0	1.6	-	V <sub>pp</sub>
Modulus Setup Time MC to Out	tset	-	11	16	ns
Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	V <sub>in</sub>	100 400		1500 1500	mVpp
Output Current (C <sub>L</sub> = 12 pF; R <sub>L</sub> = 2.2 k $\Omega$ )	IO	-	1.5	4.0	mA

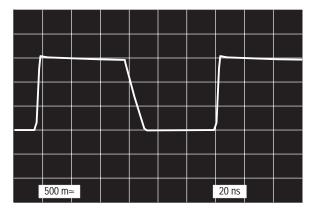


#### Figure 2. Modulus Setup Time



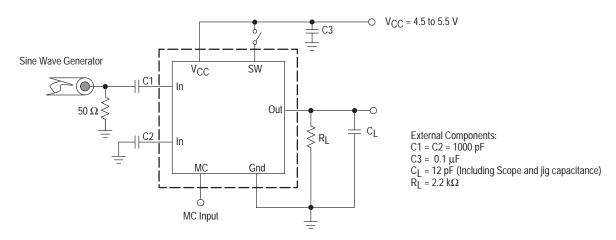
Modulus setup time MC to out is the MC setup or MC release plus the prop delay.

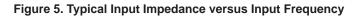
### Figure 3. Typical Output Waveform

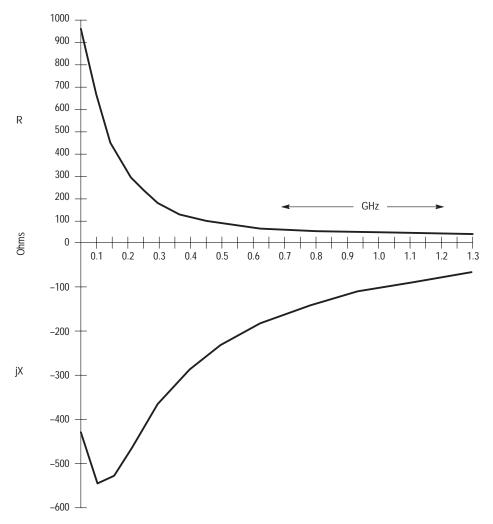


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### Figure 4. AC Test Circuit

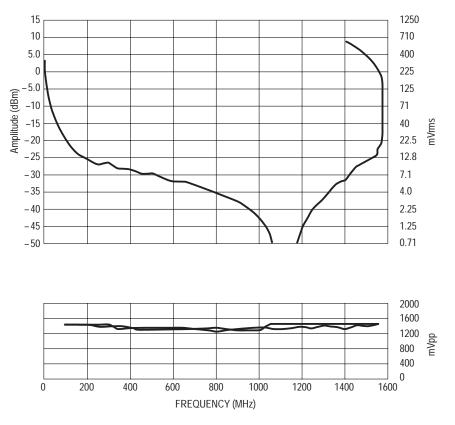




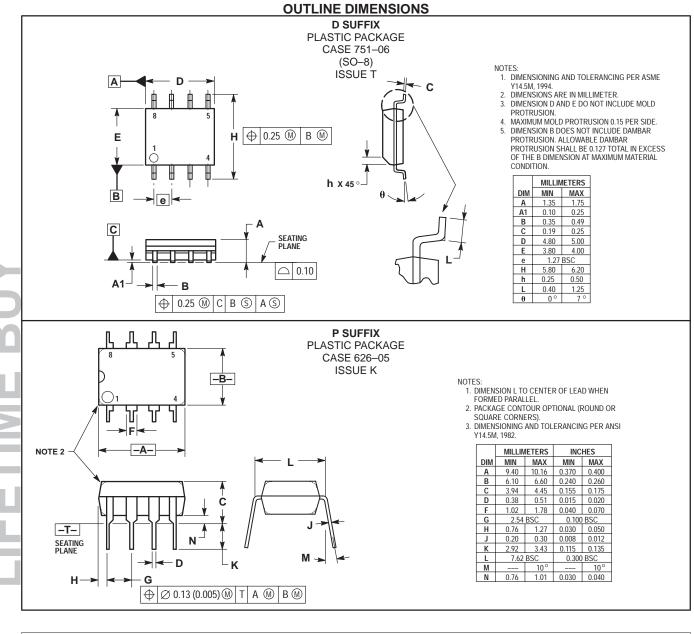


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Divide Ratio = 32



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MC12028A/D

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