

FPSLIC Prototype Kit: Using ATDH40M with Both AT94K and AT40K Devices



The existing ATDH40M prototype system can be modified to work with the AT94K Field Programmable System Level Integrated Circuit (FPSLIC™) device and the AT40K FPGA device. The ATDH40M motherboard interfaces with various daughter boards in order to program different AT94K FPSLIC/AT40K FPGA package footprints (see Table 1).

Table 1. Daughter Board Support List

Part Number	AT94K	AT40K	Description
ATDH40D84	X	X	84-pin Plastic Lead Chip Carrier
ATDH40D100	X	X	100-pin Very Thin Quad Flat Pack
ATDH40D100R		X	100-pin Rectangular Quad Flat Pack
ATDH40D144	X	X	144-pin Thin Quad Flat Pack
ATDH40D160		X	160-pin Plastic Quad Flat Pack
ATDH40D208	X	X	208-pin Plastic Quad Flat Pack
ATDH40D240		X	240-pin Plastic Quad Flat Pack

Both the AT94K FPSLIC and the AT40K FPGA devices are pin-compatible in a given package footprint across the families. Only the 84, 100,144 and 208 packages are used for FPSLIC devices.

ATDH40M Mother Board

Located below the header H1 are the mode select dip-switches DIP-SW1 that control the mode settings for the AT94K FPSLIC/AT40K FPGA devices (switches M0, M1, M2). The FPSLIC device only supports mode 0 and mode 1 with switches M0, M2. M1 is used for /AVRRESET.

All other settings will use the same procedure as our existing ATDH40M/ATDH40DXXX datasheet. This datasheet can be downloaded from Atmel's web site at <http://www.atmel.com/atmel/acrobat/doc1402.pdf>

**Programmable
SLI
AT40K
AT94K**

**Application
Note**





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